

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Raul-Adrian Cernea et al.
Title: Non-Volatile Memory and Method With Bit Line Coupled Compensation
Application No.: 10/667,222 Filing Date: September 17, 2003
Examiner: Unassigned Group Art Unit: 2818
Docket No.: SNDK.308US0 Conf. No.: 7021

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 5, 2004

Mary E. Buggin
Signature

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

This application has a filing date after June 30, 2003. Therefore copies of the U.S. Patents listed on the accompanying Form PTO-1449 are not enclosed.


Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

Attorney Docket No.: SNDK.308US0

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This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664 as set forth in the accompanying transmittal letter.

Respectfully submitted,



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February 5, 2004
Date

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U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.		Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				SNDK.308US0		10/667,222	
				Applicants		Conf. No.	
				Raul-Adrian Cernea et al.		7021	
				Filing Date		Art Group	
				September 17, 2003		2818	

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,172,338	12/15/92	Mehrotra et al.			
	2	5,570,315	10/29/96	Tanaka et al.			
	3	5,595,924	1/21/97	Yuan et al.			
	4	5,768,192	6/16/98	Eitan			
	5	5,774,397	6/30/98	Endoh et al.			
	6	5,903,495	5/11/99	Takeuchi et al.			
	7	6,011,725	1/4/00	Eitan			
	8	6,046,935	4/4/00	Takeuchi et al.			

U.S. Published Patent Application Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	9	Eitan et al., "NROM: A Novel Localized Trapping, 2-Bit Nonvolatile Memory Cell," IEEE Electron Device Letters, Vol. 21, No. 11, November 2000, pp. 543-545.

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.